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SFDLA3638F

RoHS Compliant

Specifications and Applications Information

03/23/11

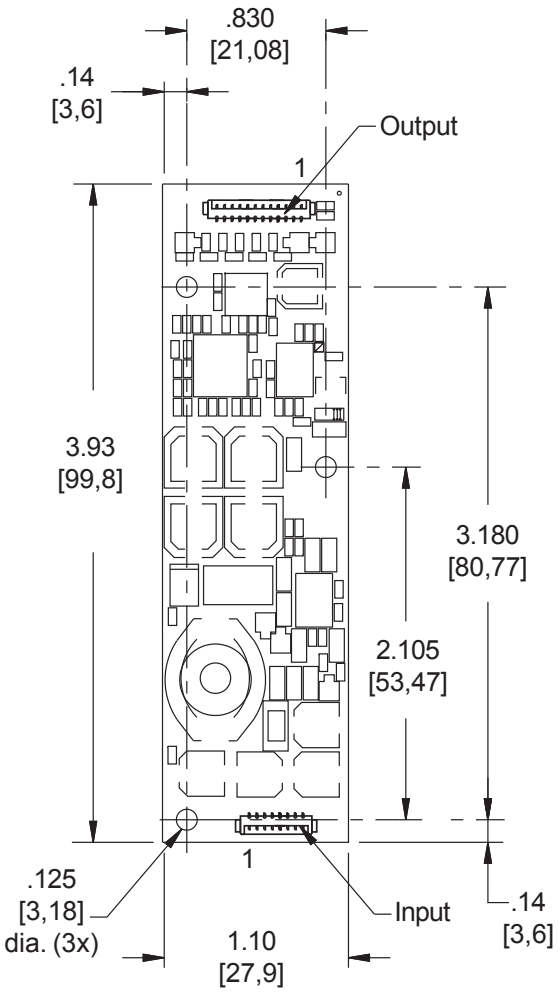
Smart Force LED Driver

The ERG **Smart Force Series** of LED Drivers are specifically designed for applications which require wide dimming and LCD brightness stability from a 5 Volt dc source. The SFDLA3638F is designed to provide backlight power for the NEC NL10276BC13-01C display.

Designed, manufactured and supported within the USA, the SFDL features:

- ✓ Less than 8 mm in height
- ✓ Constant LED current
- ✓ External or internal dimming
- ✓ High dimming ratio
- ✓ Separate enable & dimming function
- ✓ Soft start
- ✓ One year warranty

Package Configuration



PCB components are shown for reference only. Actual product may differ from that shown.

Mass: 17 grams

Connectors	
Input Connector Molex 53261-0871	**Output Connector *** Molex 53261-1271
J1-1 Vin(+) J1-2 Vin(+) J1-3 GND J1-4 GND J1-5 Enable J1-6 Vin(+) J1-7 Control J1-8 GND	J2-1 Cathode 1 J2-7 Cathode 4 J2-2 Anode 1 J2-8 Anode 4 J2-3 Cathode 2 J2-9 Cathode 5 J2-4 Anode 2 J2-10 Anode 5 J2-5 Cathode 3 J2-11 Cathode 6 J2-6 Anode 3 J2-12 Anode 6 * Requires harness: ERG part number H10312152 recommended

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Absolute Maximum Ratings

Rating	Symbol	Value	Units
Input Voltage Range	V_{in}	-0.3 to +5.5	Vdc
Storage Temperature	T_{stg}	-40 to +85	°C
Control Voltage	$V_{Control}$	0 to Vin	Vdc
Enable Input Voltage	V_{Enable}	0 to Vin	Vdc

Operating Characteristics

Unless otherwise noted Vin = 5.00 Volts dc and Ta = 25°C.

Characteristic	Symbol	Min	Typ	Max	Units
Input Voltage	V_{in}	+4.6	+5.0	+5.5	Vdc
Component Surface Temperature (Note 1)	T_s	-20	-	+80	°C
Input Current	I_{in}	0.68	0.80	0.92	Adc
LED String Voltage	V_{LED}	26.0	28.8	32.3	Vdc
Efficiency	η	-	70	-	%
Output Current (per string)	I_{out}	15.3	16.2	17.0	mAdc
Enable Pin (Note 2)					
Turn-on Threshold	V_{thon}	-	-	1.55	Vdc
Turn-off Threshold	V_{thoff}	1.25	-	-	Vdc
Enable Input Impedance	R_{Enable}	-	5.0	-	kOhms
Control Pin					
Full-on Threshold	V_{fon}	-	0.9	-	Vdc
Full-off Threshold	V_{foff}	-	4.2	-	Vdc

Specifications subject to change without notice.

Note 1 Surface temperature must not exceed 80°C, except for U5 which must be kept below 100°C; thermal management actions may be required.

Note 2 The input voltage to the driver must be within its operating characteristic before the driver is enabled, otherwise the driver may not start or may shut down unexpectedly.

Onboard PWM

Unless otherwise noted Vin = 5.00 Volts DC, T_a = 25 °C and unit has been running for 5 minutes.

Characteristic	Symbol	Min	Typ	Max	Units
Frequency	f_{pwm}	-	160	-	H_Z
Control Input Bias Current	I_{cbias}	-	-	10	uA

Application Information

The ERG SFDLA3638F has been designed to be configured in multiple ways:

NO DIMMING

- OPERATION: The SFD driver can be configured to operate without dimming by floating the Control Pin (J1-7), and the Enable Pin (J1-5).
- Pins 1 and 2 of connector J1 must be connected to +Vin, between 4.6 and 5.5 Vdc. Pins 3 and 4 of connector J1 must be connected to GND.

ONBOARD PWM DIMMING

- OPERATION: Onboard PWM configuration as shown in Figure 1 allows the user to control display brightness by controlling the onboard PWM generator. The user is responsible to provide an analog control signal. A dimming ratio up to 1000:1 is possible with this configuration.
- DIMMING: Dimming is accomplished by applying an analog voltage to the Control Pin (J1-7). Display brightness is modulated by controlling the Control Pin voltage as shown in Graph 1.
- ENABLE/DISABLE: The driver may be enabled or disabled (turned on and off) by applying a DC voltage to the Enable Pin(J1-5). Enable Pin on and off levels are specified in the Operating Characteristics section of the data sheet. The driver can also be enabled by floating the Enable Pin.
- Pins 1 and 2 of connector J1 must be connected to +Vin, between 4.6 and 5.5 Vdc. Pins 3 and 4 of connector J1 must be connected to GND.

EXTERNAL PWM DIMMING

- OPERATION: External PWM configuration as shown in Figure 2 allows the user to control display brightness with an externally generated PWM signal. The user is responsible to provide the PWM signal. A dimming ratio up to 5000:1 is possible with this configuration.
- DIMMING: Dimming is accomplished by applying a PWM signal to the Enable Pin (J1-5). PWM on and off levels are specified in the Operating Characteristics section of the data sheet. Display brightness is modulated by controlling the PWM duty cycle as shown in Graph 2.
- ENABLE/DISABLE: The driver may be enabled or disabled (turned on and off) by applying a DC voltage to the Control Pin (J1-7). Control Pin on and off levels are specified in the Operating Characteristics section of the data sheet. The driver can also be enabled by floating the Control pin.
- Pins 1 and 2 of connector J1 must be connected to +Vin, between 4.6 and 5.5 Vdc. Pins 3 and 4 of connector J1 must be connected to GND.

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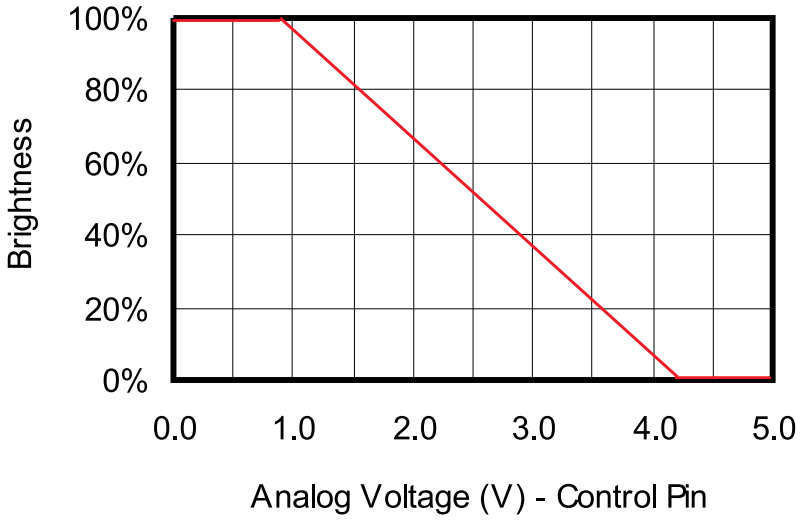
ONBOARD PWM DIMMING



Graph 1

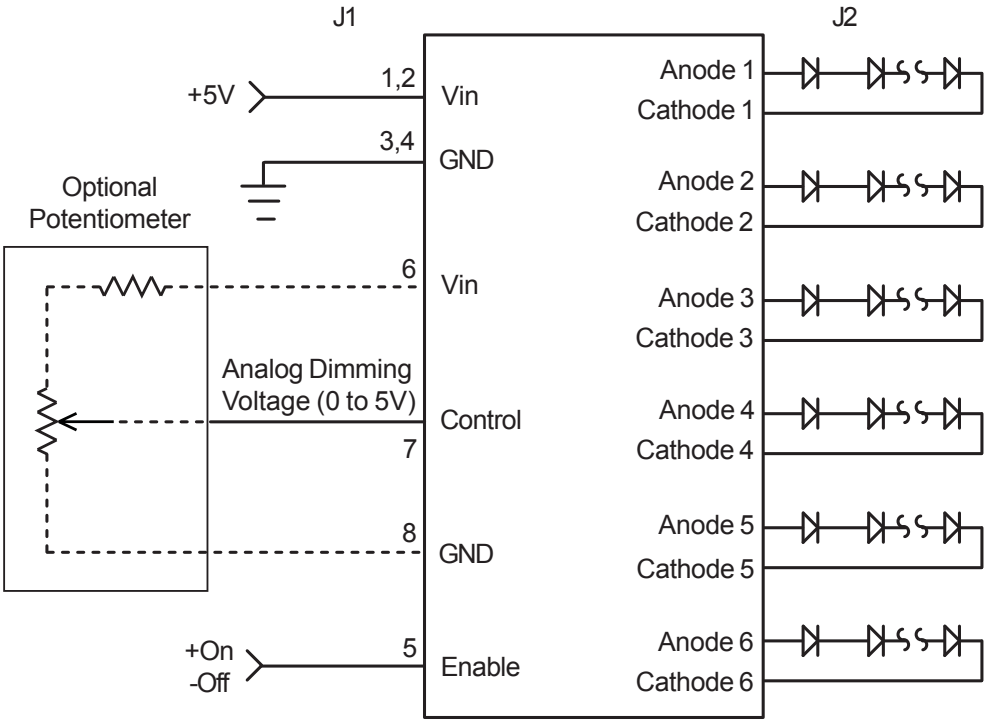


Figure 1

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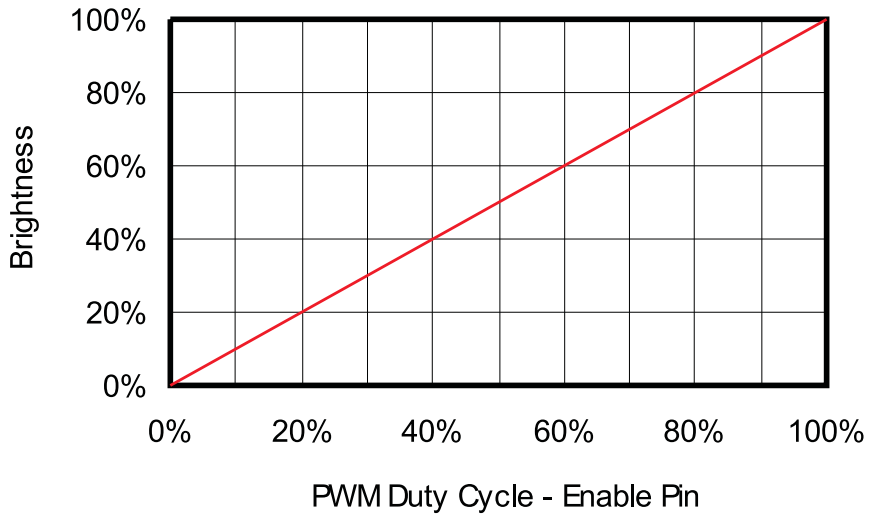
EXTERNAL PWM DIMMING



Graph 2

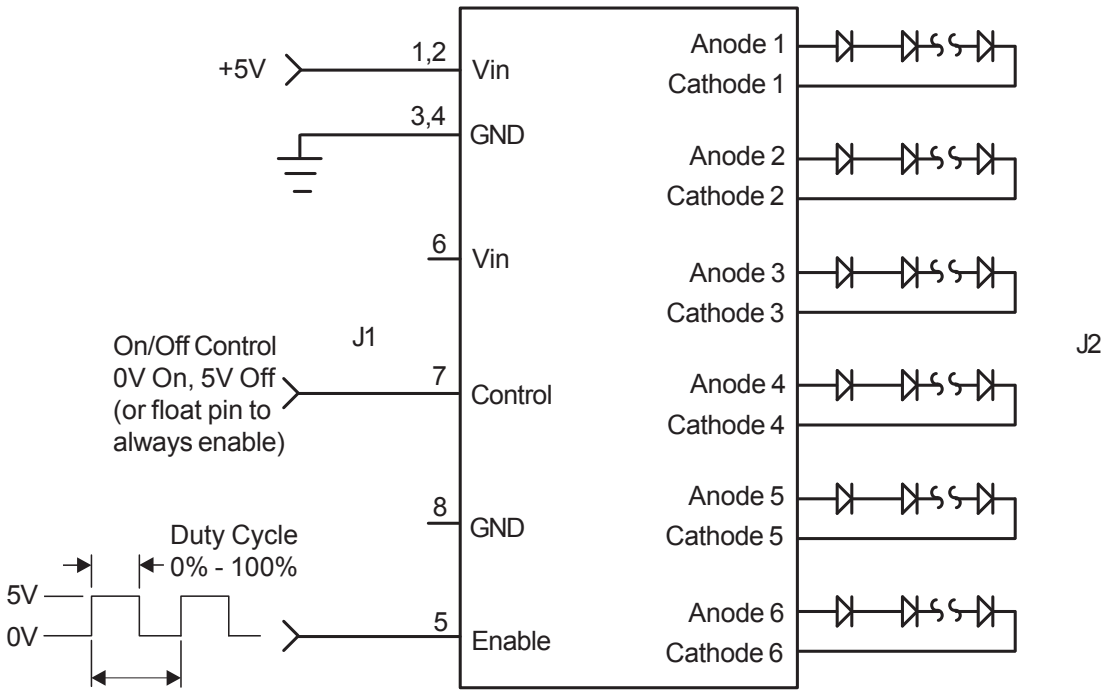


Figure 2

Endicott Research Group, Inc. (ERG) reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by ERG is believed to be accurate and reliable. However, no responsibility is assumed by ERG for its use.