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http://www.ergpower.com

### Specifications and Applications Information

12/27/06 **Preliminary** 

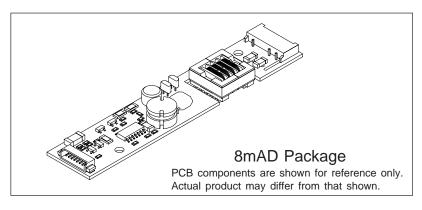
The ERG 8mAD2872 (8m Series) DC to AC inverter features onboard connectors and can be easily dimmed using an external analog control signal or external PWM generator.

Powered by a regulated +12 Volt DC source, the 8mAD2872 is designed to power the Toshiba LTM08C351 display backlight.

#### **Product Features**

- Small Package Size, less than 9mm in height.
- **High Dimming Ratio**
- High Efficiency
- Made in U.S.A.

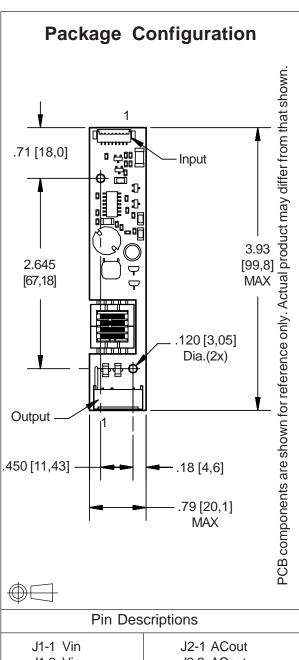
This unit complements our 8m Series of DC to AC Inverters



<u>Connectors</u>				
Input J1	Output J2			
Molex 53261-0871	JST SM04(4.0)B-BHS-1-TB			

## 8mAD2872

Two Lamp DC to AC Inverter



J1-1 Vin J1-2 Vin J1-3 GND J1-4 GND J1-5 Enable J1-6 Control J1-7 N/C J1-8 GND J2-1 ACout J2-2 ACout J2-3 N/C J2-4 ACcommon



#### **Absolute Maximum Ratings**

Rating	Symbol	Value	Units
Input Voltage Range	V <sub>in</sub>	-0.3 to +13.2	Vdc
Storage Temperature	T <sub>stg</sub>	-40 to +85	°C

#### **Operating Characteristics**

With a load simulating the referenced display and lamp warm-up of 5 minutes. Unless otherwise noted Vin = 12.00 Volts dc and  $Ta = 25^{\circ}\text{C}$ .

Characteristic	Symbol	Min	Тур	Max	Units
Input Voltage	V <sub>in</sub>	+10.8	+12.0	+12.6	Vdc
Component Surface Temperature	Ts	-20		+80	°C
Input Current (Note 1)	l in	-	0.49	0.57	Adc
Input Ripple Current	l rip	-	20	-	mA <sub>pk-pk</sub>
Operating Frequency	F <sub>o</sub>	40	45	50	kHz
Minimum Output Voltage	Vout (min)	1500	-	-	Vrms
Efficiency	η	-	75	-	%
Output Current (per lamp)	I out	-	6.3	-	mArms
Output Voltage	Vout	-	350	-	Vrms
Enable Pin					
Turn-off Threshold	V thoff	GND	-	0.8	Vdc
Turn-on Threshold	V thon	2.0	-	Vin	Vdc
Impedance to Vin	R <sub>Enable</sub>	44.6	47.0	49.4	kOhms

Specifications subject to change without notice.

(Note 1) Input current in excess of maximum may indicate a load/inverter mismatch condition, which can result in reduced reliability. Please contact ERG technical support.

#### **Application Notes:**

- 1) The minimum distance from high voltage areas of the inverter to any conductive material should be .12 inches per kilovolt of starting voltage.
- 2) Mounting hardware should be non-conductive.
- 3) Open framed inverters should not be used in applications at altitudes over 10,000 feet.
- 4) Contact ERG for possible exceptions.



#### **Onboard PWM**

Unless otherwise noted Vin = 12.00 Volts DC, Ta = 25 °C and unit has been running for 5 minutes.

Characteristic	Symbol	Min	Тур	Max	Units
Frequency	f <sub>pwm</sub>	-	160	-	Hz
Control Input Bias Current	I chias	-	-	10	uA

#### **Pin Descriptions**

**Vin** Input voltage to the inverter.

**GND** Inverter ground.

Control Analog voltage input to the onboard pulse width modulator. Increasing this voltage increases the off

time of the onboard PWM resulting in decreased brightness. The inverter is full on when this voltage

is near inverter ground.

**Enable** Inverter Enable. The inverter is always enabled with an internal pullup resistor tied to the enable pin.

Pull this pin low to disable inverter operation. The onboard PWM is always utilized.

#### **Application information**

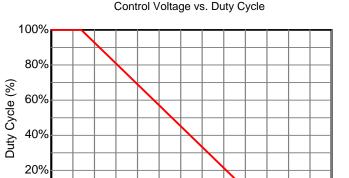
The 8mAD series of inverters is designed to power two cold cathode fluorescent lamps each with four watts. An external analog control interfaces with an onboard pulse width modulator to provide dimming control. The 8mAD inverter can reliably dim to less than 5% duty cycle.

External shutdown of the inverter is accomplished using the Enable pin. Pulling this pin low (below Vthoff) disables the inverter.

If analog voltage dimming is required, the analog voltage is applied to the Control pin. Figure 1 shows how to connect the inverter for onboard PWM operation. Graph 1shows the relationship of PWM duty cycle to input control voltage.

If an external PWM is used, simply connect the Enable pin to the PWM source and connect the Control pin to inverter ground. If the onboard PWM is used, connect the analog voltage to the Control pin.





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1

2

Graph 1

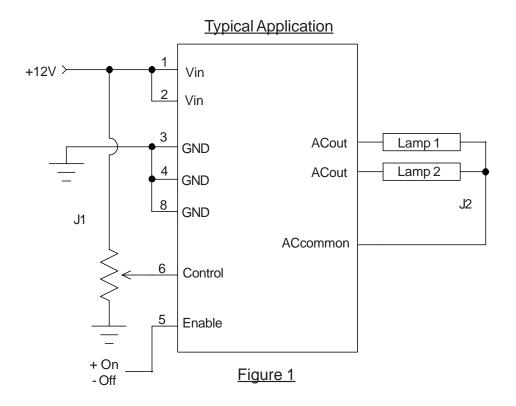
3

Control Voltage (V)

4

5

6





Endicott Research Group, Inc. (ERG) reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by ERG is believed to be accurate and reliable. However, no responsibility is assumed by ERG for its use.